

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph on page 26, beginning at line 7 as follows:

By way of example, any of the multiplexers (selection circuits) discussed herein, such as for example multiplexers 86, 88, 90, 92, 94, 96, 98, 100, 102, 104, may comprise any of the selection circuits described in copending U.S. Patent Application No. 09/250,781, filed February 12, 1999, now abandoned, entitled “MULTI-DIMENSIONAL PROGRAMMABLE INPUT SELECTION APPARATUS AND METHOD”, invented by Chung-Yuan Sun, and commonly assigned herewith, the full disclosure of which is hereby incorporated into the present application by reference.

Please amend the paragraph starting on page 35, beginning at line 19 as follows:

The full disclosures of the following copending U.S. patent applications are hereby incorporated into the present application by reference: U.S. Patent Application No. 09/231,998, filed January 15, 1999, now abandoned entitled “STORAGE CELLS UTILIZING REDUCED PASS GATE VOLTAGES FOR READ AND WRITE OPERATIONS”, and commonly assigned herewith; U.S. Patent Application No. 09/281,008, filed March 30, 1999, now issued as U. S. Patent No. 6,301,696, entitled “METHOD AND APPARATUS FOR INCREASING THE SPEED OF FIELD-PROGRAMMABLE GATE ARRAYS”, and commonly assigned herewith; U.S. Patent Application No. 09/285,563, filed April 2, 1999, now issued as U.S. Patent No. 6,446,242, entitled “METHOD AND APPARATUS FOR STORING A VALIDATION NUMBER IN A FIELD-PROGRAMMABLE GATE ARRAY”, and commonly assigned herewith; and

U.S. Patent Application No. 09/318,198, filed May 25, 1999, now issued as U.S. Patent No. 6,211,697, entitled “INTEGRATED CIRCUIT THAT INCLUDES A FIELD-PROGRAMMABLE GATE ARRAY AND A HARD GATE ARRAY HAVING THE SAME UNDERLYING STRUCTURE”, and commonly assigned herewith.